

## REPORT ON PCB DESIGN CHALLENGE COMPETITION

Department of Electronics and Communication Engineering organized a club activity "PCB DESIGN CHALLENGE COMPETITION" under CoE in Robotics and Industrial Automation. In PCB Design challenge competition, students had to create Schematic Design, PCB Layout and Extract the Gerber file corresponding to the given circuit challenge . It was conducted and coordinated by Mr. L. David William Raj (AP/ECE) and Dr.Utsab Banerjee (AP/ECE) . There were 30 participants from different streams of 2<sup>nd</sup>, 4<sup>th</sup> Semester.

The program started on 22<sup>nd</sup> July 2023 at 01.00 PM. Prof. Murali C , Former Vice President, IETE, New Delhi was the guest for the programme.

The Competiton started with an introduction of the Chief guest. Prof. C Murali, Former Vice President, IETE, New Delhi, explained the basic rules and regulations, followed by providing the details of the circuit to be designed.



Participants preparing the schematic designs under the close supervision of Prof. C. Murali, Former Vice President, IETE, New Delhi.

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Accredited by NBA & NAAC

The competition started with the students creating the schematic PCB Design of the given circuits. The students participating in the competition came with Pre installed EDA tools like Cadence Allegro/ Eagle CAD/ Tinker CAD/ Multisim/ Proteus.



Participants busy in preparing layout

The single Layer PCB Schematic design was validated by the chief guest. After Validation the students were instructed to extract the Gerber file for their design schematics. The students got an opportunity to apply their technical knowledge in creating practical designs.

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Winner of the PCB Design Challenge competition organized by Robo Lab on 22.07.2023



Runners of the PCB Design Challenge competition organized by Robo Lab on 22.07.2023

In addition to the Winners, certificates were issued to all the registered participants.

### Winners

1. Ashish Ramesh (1MJ22CS021)
2. Jonathan Daniel (1MJ22IO008)

### Runners

1. Karthik Gururaj Badami  
(1MJ21EC054)
2. Avala Ganesh Yadav  
(1MJ21EC012)

### Outcomes:

1. Students designed the circuit using Cadence Allegro/ Eagle CAD/ Tinker CAD.
2. Students extracted the Gerber file for their schematic design.
3. Students gained valuable experience in designing and extracting electrical circuits.